

## **Hardware Engineering Internships with ARM**

We work in small to medium-sized teams with most following modern Agile principles. Engineers share ideas and add to the ideas of others, document and present their work for discussion, review and support the efforts of others, whilst sharing their findings impartially and authoritatively.

In your role, you will be introduced to the teams, our ways of working, be treated liked any other engineer on the team and support by experienced engineers.

### **What you could be doing as a Hardware Intern:**

- Working with design teams to develop IP that delivers high performance, power efficient products.
- Analysing existing tools looking for enhancements/automation, alongside trialling new tools.
- Verifying IP using a wide range of methodologies – constrained random simulation using testbenches written in SystemVerilog, running real applications on emulation or FPGA platforms, and using formal methods.
- Implementing Arm IP in silicon process nodes using design automation tools.
- Developing system solutions using ARM IP for different market domains ranging from mobile, IoT, data centres, etc.
- Writing specifications for Arm's IP products and systems – analysing trade-offs between different options using software or hardware models.
- Providing Verification and Implementation support for systems through the lifecycle of the design right up to the delivery to the customer.

### **We're looking for individuals who are:**

You will need to be studying towards a degree in Electronic Engineering, Computer Engineering, Computer Science or any other relevant subject. Other degree types may be considered with relevant experience.

### **Qualities that will help your application stand out:**

- Experience in at least one programming language.
- A real passion for computing and/or the semi-conductor industry that goes beyond your studies.
- Good attention to detail with the ability to problem solve and express ideas optimally.
- Team-spirit with an appreciation for the "We, not I" core belief.
- If you have an interest in computer architecture fundamentals, digital design concepts, CPU architecture and microarchitecture features (such as caches, MMU, SMP, coherency, CPU pipelines) this is a plus, but not essential.
- Any familiarity with a hardware description language like VHDL or Verilog/SystemVerilog is also helpful.