

**SS ELECTRONIC / ELECTRONIC & COMPUTER / COMPUTER ENGINEERING (CCDD), 2024/25 - TIMETABLE (Semester 2)**

DAY	0900 - 1000	1000 - 1100	1100 - 1200	1200 - 1300	1300 - 1400	1400 - 1500	1500 - 1600	1600 - 1700	1700 - 1800
MONDAY	4C8 [M17]		CSU44054 [LB04]	4C21 [M21]	4C8 [CLT] CSU34041 [TH]			CSU44032 [LB01]	
TUESDAY	4C8 [SYNGE, HAM]	4B9 Tutorial [HLT]	CSU44D02 [M21]	4C8 Laboratories [CADLAB]		4B9 [LB04]		4B9 [M17]	
WEDNESDAY	4B12 [DO]		4B9 [CLT]	CSU44054 [LB08]	CSU44054 [L2.01, L2.02] 4B9 [M21]	CSU44D02 [M21]		CSU44032 [LB04] CSU34041 [MacNeil]	
THURSDAY			4C21 Lecture/Lab [CADLAB]			4C2 Lab [AP2.28]	4C2 Lab [Wks 4,8,11 AP0.12]		4B12 Tutorial [DO]
FRIDAY			Electronic Engineering laboratories/projects (C and CD stream) [EE LABS]		4C2 [AP2.03]	CSU44D02 [1.07]	4B9 laboratories [MECH LAB] 4C2 [AP2.03 weeks 1-6 & 8 AP2.04 weeks 9-12]		

**Module Codes:**

4E1 = CEU44E01 Management for Engineers [5 credits]  
 4E2 = EEU44E02/CS4E2 Electronic/Computer Engineering Project [15 credits]\*  
 4E3 = EEU44E03 Research Methods [5 credits]  
 4B9 = MEU44B09 Control Engineering I [5 credits]  
 4B12 = Introductio to Autonomous Mobile Robotics [5 credits]  
 4C1 = EEU44C01 Integrated Systems Design [5 credits]  
 4C2 = EEU44C02 Microelectronics [5 credits]  
 4C4 = EEU44C04 Next Generation Networks [5 credits]  
 4C5 = EEU44C05 Digital Signal Processing [5 credits]  
 4C7 = EEU45C07 Self-Organising Systems Theory [5 credits]  
 4C8 = EEU44C08 Digital Image and Video Processing [5 credits]  
 4C15 = EEU44C15 Analogue Signal Processing [5 credits]  
 4C16 = EEU44C16 Deep Learning and its Applications [10 credits]  
 4C21 = Open Reconfigurable Networks [5 credits]  
 CSU34041 = Information Management II (CS4D2A) [5 credits]  
 CSU34201 = Computer Architecture II (CS3421) [5 credits]  
 CSU44000 = Internet Applications (CS4400) [5 credits]  
 CSU44032 = Security and Privacy (CS4407) [5 credits]  
 CSU44052 = Computer Graphics (CS4052) [5 credits]  
 CSU44053 = Computer Vision (CS4053) [5 credits]  
 CSU44054 = Augmented Reality (CS7434) [5 credits]  
 CSU44056 = Data Visualisation (CS4406) [5 credits]  
 CSU44D02 = Knowledge Engineering (CS4D2B) [5 credits]

**Venues:**

CADLAB= Room 2.28, Aras an Phiarsaigh  
 LG35, 36, 37 = PC Labs, O'Reilly Building  
 M20 = M20, Museum Building  
 M21 = M21, Museum Building  
 AP2.04 = Aras an Phiarsigh  
 LB01, LB04, LB08 = Lloyd Inst.  
 M17 = M17, Museum Building  
 Drawing Office = Drawing Office, Museum Building  
 L2.02 = Trinity Central  
 CLT = Crossland Lecture Theatre, Parsons Building  
 Demo Room = Simon Perry Building  
 2043 = Arts Block  
 1.07 = Lloyd Inst.  
 UGLAB = 2.15 Aras an Phiarsigh  
 Syngé Theatre = Hamilton Building  
 TH = Tercentenary Hall, TBSI, Pearse St  
 HLT = Haughton Lecture Theatre, Museum Building  
 CADLAB= AP2.28 Aras an Phiarsigh

**Semester dates:**

First semester: Monday, 9th September, 2024 to Friday, 29th November, 2024  
 Second semester: Monday, 20th January, 2025 to Friday, 11th April 2025

**Study/Review Weeks:**

First semester: Monday, 21st October 2024 to Friday, 25th October 2024  
 Second semester: Monday, 3rd March 2025 to Friday, 7th March 2025

**Examination dates:**

**Semester 1 examinations:**  
 Monday, 9th December 2024 to Friday, 13th December 2024\*  
 (\*contingency days may be required outside of the formal assessment weeks)

**Semester 2 examinations:**

Monday, 21st April, 2025 to Friday, 25th April, 2025\*\*  
 (\*\*contingency days may be required outside of the formal assessment weeks)

**Reassessment session:**

To be confirmed

**Laboratories and Tutorials**

Please consult schedule

\* For students who are completing their studies in the Senior Sophister BAI year, they MUST undertake a Computer/Electronic Engineering Project - the option of taking an internship is only available to students proceeding to the MAI year 5.