

Module Template for New and Revised Modules¹

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| Module Code | EEU44C05 |
| Module Name | DIGITAL SIGNAL PROCESSING |
| ECTS Weighting² | 5 ECTS |
| Semester taught | Semester 1 |
| Module Coordinator/s | Professor Naomi Harte |
| <u>Module Learning Outcomes</u> with reference to the <u>Graduate Attributes</u> and how they are developed in discipline | <p>On successful completion of this module, students should be able to:</p> <ul style="list-style-type: none">LO1. Outline and use a variety of approaches to sampling and reconstruction of signals .LO2. Describe, appraise and implement filter design methods for IIR and FIR filters, identifying trade-offs and evaluating outcomes.LO3. Appreciate and illustrate the role of linear phase.LO4. Elaborate on the relationship between the Continuous Time Fourier Transform and the Discrete Fourier Transform.LO5. Discuss the importance and relevance of properties of the DFT.LO6. Illustrate fast algorithms for implementation of the DFT and their practical use and advantages.LO7. Interpret and analyse signals using spectral analysis techniques derived from the DFT.LO8. Appreciate the role of signal processing in current approaches to system design.LO9. Exploit assigned reading and lab exercises to deepen insights into module content. <p>Graduate Attributes: levels of attainment</p> <p>To act responsibly - Enhanced To think independently - Attained To develop continuously - Attained To communicate effectively - Attained</p> |

¹ [An Introduction to Module Design](#) from AISHE provides a great deal of information on designing and re-designing modules.

² [TEP Glossary](#)

Module Content

Signal Processing is concerned with the representation, transformation and manipulation of signals and the information they contain. Typical signals include speech, video, measurements of physical phenomena such as pressure, speed, or information of all kinds from stock market prices to mobile phone text messages. This module deals specifically with the treatment of signals that are digital. This means that they are discrete in time, i.e. sampled, often from a signal that was originally continuous or analog, and they are also quantised, taking on one of a fixed set of values, and often represented in binary form in implementation. Hence the module title – Digital Signal Processing. The module includes the following content:

- Signals and Properties, LTI systems – a review (3C1 or equivalent background)
- Sampling and reconstruction, Decimation, interpolation, quantisation
- Digital Filters, Linear Phase systems
- Filter design methods – IIR, FIR
- Optimum FIR Filter Design
- Practical Filter Design in Matlab
- Discrete Fourier Series, Discrete Fourier Transform, Discrete Fourier Transform Properties
- Computation of FFT
- Random Processes
- Spectral analysis of signals with DFT
- Filter realisations
- DSP in an era of Deep Learning

Teaching and Learning Methods

The module is delivered with:

- 3 lectures weekly
- 1 tutorial weekly from Week 3 (with homework assigned a week in advance)
- 4 Laboratories (mostly self-directed, done in Matlab, with a 1 hour clinic provided per student per lab)
- The delivery will include a number of guest lectures to allow students hear about DSP in action in real-world applications.
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Students are expected to engage fully with the module with self-directed study of the supporting module text or other suggested reading. This is

essential to aid their understanding of the content. The labs will be used to reinforce concepts delivered through the lecture program. Tutorial problems will be set weekly, aligned with module content. The tutorial session will be run in a manner which expects students to have completed the homework prior to attendance.

| Assessment Details³ Please include the following: <ul style="list-style-type: none"> • Assessment Component • Assessment description • Learning Outcome(s) addressed • % of total • Assessment due date | Assessment Component | Assessment Description | LO Addressed | % of total | Week due |
|---|----------------------|------------------------------------|--------------|------------|--|
| | Annual Exam | End of year written in-person exam | All | 85 | End of Semester |
| | CA | 4 labs | 1,2,3,7,9 | 15 | Date depends on group. 2 labs from Week 3-7, 2 Labs from Week 8-11 |
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Reassessment Requirements

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| Contact Hours and Indicative Student Workload³ | Contact hours: 46 |
| | Independent Study (preparation for course and review of materials): 42 |
| | Independent Study (preparation for assessment, incl. completion of assessment): 20 |

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|---------------------------------|---|
| Recommended Reading List | Discrete-Time Signal Processing, Alan V. Oppenheim, Ronald W. Schafer (3rd edition) |
| Module Pre-requisite | EE3C1 or equivalent |
| Module Co-requisite | |
| Module Website | Blackboard |

³ [TEP Guidelines on Workload and Assessment](#)

Are other Schools/Departments involved in the delivery of this module? If yes, please provide details.

Module Approval Date

Approved by

Academic Start Year

September 2023

Academic Year of Date

2023-24