Module Code	EEU22E10		
Module Name	ENGINEERING DESIGN IV: PROJECT		
ECTS Weighting ²	10 ECTS		
Semester taught	Semester 2		
Module Coordinator/s	Prof. Harun Siljak, Prof. Glenn Strong		
Module Learning Outcomes with reference to the <u>Graduate Attributes</u> and how they are developed in discipline	 On successful completion of this module, students should be able to: Apply the engineering process of problem solving. Design a simple autonomous vehicle to meet a well-defined specification. Clearly demonstrate group working, including task sub-division and integration of individual contributions from the team. Plan a project, meeting all interim deliverables. Implement project tracking and code version control. Apply knowledge of the health and safety requirements of electronic circuit board construction. Recognise issues to be addressed in a combined hardware and software system design. Develop skills in the areas of quantitative analysis, scientific reasoning and communication. Develop practical experimental skills in electronic circuit testing. Develop practical experimental skills in software system testing. Evaluate the outcome of their achievements given theoriginal specification. Demonstrate organised and concise report writingskills. Graduate Attributes: levels of attainment To act responsibly - Introduced To think independently - Introduced To develop continuously - Introduced To communicate effectively - Introduced 		

¹ <u>An Introduction to Module Design</u> from AISHE provides a great deal of information on designing and re-designing modules.

² <u>TEP Glossary</u>

Module Content	 The objectives of 1. To apply be Design, Im 2. To introdue 3. To introdue safety issue adoption of 4. To introdue user interfactor 5. To analyse manufacte 6. To introdue drawings a 7. To introdue 	f this module ar pasic principles oplement and O uce group working uce the principle ues associated work of test procedur uce the principle face design and cations; the design and cations; the design and cations and test uce the requirer and software do uce project repo	re: of science and e perate (CDIO) a ing and project p es of circuit cons with electronic o res; es of software sy control softwar d optimise it with sting; ments of project ocumentation; orting and presen	ngineering to Con n autonomous ve planning; truction and the circuit construction vstems design inco e for wireless h respect to documentation, ntation.	nceive, hicle; health and on and the luding circuit
Teaching and Learning Methods	The module is taught using a combination of lectures, demonstration laboratories and through project sessions at which advisors are present. As a 10 ECTS course, the average individual student effort should be 200- 250 hours spread over the semester. 55 of these will be actual contact hours. Thus all students, as individuals and as groups, are also expected to undertake extensive independent research and development work on the project.				
Assessment Details ³ Please include the following: • Assessment Component • Assessment description • Learning Outcome(s) addressed • % of total	Assessment Component	Assessment Description	LO Addressed	% of total	Week due
	Continuous Assessment	combination of demos, interviews,	all	100%	Througho ut the

tests and

reports

semester

- % of total •
- Assessment due date •

Reassessment Requirements

Contact Hours and Indicative Student Workload ³	Contact hours: 50
	Independent Study (preparation for course and review of
	materials): 50
	Independent Study (preparation for assessment, incl.
	completion of assessment): 150

³ TEP Guidelines on Workload and Assessment

Recommended Reading ListModule Pre-requisiteModule Co-requisiteModule WebsiteAre other Schools/Departments
involved in the delivery of this module?Module Approval DateApproved byAcademic Start YearAcademic Year of Date

September 2024